



Embedded Systems Engineer

Job Description

As an Embedded Systems Engineer, you will be part of an ambitious project that aims at improving 4G/ 5G wireless communication network power consumption. You will work alongside the system architect to establish the requirements for the IPs you'll develop. We're a small company so your impact will be wide and lasting.

Your responsibilities would be the following:

- Design and implement IPs on FPGA or SoC.
- Definition of tests procedures.
- Validation and documentation of the tests procedures.
- Technical specifications and documentation.



In a diverse and rich hi-tech environment, you will evolve in a mixed-skills team composed of RF & mmWave, Analog, RF Power & System Architect engineers.

By joining us, you will take part of exciting and innovative projects, with real opportunities to grow your career as Wupatec develops its business.

Position is based in Limoges/ France, an affordable mid-size city at the heart of France where quality of life is among highest in Europe.

Key Competencies Required:

- Bachelor/Master Degree in electronics engineering with focus on Embedded System or Computer Science.
- 4+ years' experience in R&D of FPGA/ SoC embedded systems.
- Deep knowledge of FPGA design and programming in Verilog/ VHDL/ Embedded C-C++ programming, including simulation, verification and debugging.
- Ability to generate and test independent modules using available cores and integrate these modules into the current work.
- Experience with Vivado/ Quartus development platform.
- Must be experienced with digital signal processing (numerical filter, DSP, FFT...) and master Matlab.
- Excellent problem solving skills with a proactive attitude.
- Excellent verbal and written communication and presentation skills.
- National and International travel to customer(s) site or partner(s).



Desired Competencies:

- Familiarity with Xilinx Zynq Ultrascale+.
- Familiarity with JESD204B interface.



About Wupatec

Founded in 2016 and located in Limoges, France, Wupatec has developed unique IPs to significantly decrease the power consumption of RF Front end. With the ever increasing need for broadband data (LTE/5G handset, LTE/5G eMBB, Public Safety Network ...), the power hungry and narrow band Radio Front End Amplifier is a major roadblock to finally unleash all of the benefits of true Software Defined Radio (SDR), and the deployment of 5G networks with his large Massive MIMO Antenna System.

Wupatec unique architecture of its envelope tracking (ET) modulator, coupled with mainstream GaN Process delivers an ultra-compact and cost effective solution, with outstanding performances, a major breakthrough not uncounted in RF front end in the past 2 decades. Moreover, the uniqueness of ET implementation enable broadband amplifiers, needed to support Inter-Bands Carriers Aggregation, corner stone of 5G.

Starting date : as soon as possible

Contract type : full time

Location: Limoges, France

How to apply

Send resume and motivation letter to the attention of Stephane Dellier : dellier@wupatec.com

